

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	104	(memory adj controller) and crossbar and (external adj processor)	US-PGPUB; USPAT; USOCR	ADJ	ON	2005/12/22 10:09
L2	58	1 and core	US-PGPUB; USPAT; USOCR	ADJ	ON	2005/12/22 10:14
L3	17441	(second or two or multiple) adj core	US-PGPUB; USPAT; USOCR	ADJ	ON	2005/12/22 11:14
L4	192	3 and (memory adj controller)	US-PGPUB; USPAT; USOCR	ADJ	ON	2005/12/22 11:15
L5	14	4 and crossbar	US-PGPUB; USPAT; USOCR	ADJ	ON	2005/12/22 11:15
L6	37941	multiprocessor or (multi adj processor) or (multiple adj processor)	US-PGPUB; USPAT; USOCR	ADJ	ON	2005/12/22 10:46
L7	99	6 and 4	US-PGPUB; USPAT; USOCR	ADJ	ON	2005/12/22 10:46
L8	33958	(second or two or multiple) adj2 core	US-PGPUB; USPAT; USOCR	ADJ	ON	2005/12/22 11:21
L9	483	8 and (memory adj controller)	US-PGPUB; USPAT; USOCR	ADJ	ON	2005/12/22 11:15
L10	35	9 and crossbar	US-PGPUB; USPAT; USOCR	ADJ	ON	2005/12/22 11:15
L11	21	10 not 5	US-PGPUB; USPAT; USOCR	ADJ	ON	2005/12/22 11:15
L12	14	(second or two or multiple) adj2 core adj2 single adj chip	US-PGPUB; USPAT; USOCR	ADJ	ON	2005/12/22 11:21
L13	190658	(set adj top) or television	US-PGPUB; USPAT; USOCR	ADJ	ON	2005/12/22 12:10
L14	1035	13 and 8	US-PGPUB; USPAT; USOCR	ADJ	ON	2005/12/22 12:10
L15	621	13 and 3	US-PGPUB; USPAT; USOCR	ADJ	ON	2005/12/22 12:10

L16	112	15 and g06f\$.ipc.	US-PGPUB; USPAT; USOCR	ADJ	ON	2005/12/22 12:11
L17	81	16 and embedded	US-PGPUB; USPAT; USOCR	ADJ	ON	2005/12/22 12:21
L18	1	"5036458".PN.	USPAT; USOCR	ADJ	ON	2005/12/22 12:22